# Simulation and Study of AC-AC Converter with Improved Power Quality

# Prof. Sanjay R. Patel | Prof. Nigam K. Prajapati

<sup>1</sup>(Electrical Engineering Department, GTU, Ahmedabad, India, sanjaypatel265@gmail.com) <sup>2</sup>(Electrical Engineering Department, GTU, Ahmedabad, India, nigamprajapati.ee@hcet.gtc.ac.in)

**Abstract**—Purpose of this paper is to simulate and study an "AC-AC Converter" with improved input power quality and its output parameters. Reduced Input current harmonics and enhanced input power factor are met by involving active front end converter. Output stability, repeatability and its real time control in dynamic situation were prime parameters to be designed. Power topology includes double power conversion, viz. AC to DC and DC to AC. A PWM control method is selected for both power conversions. In this paper, a full focus is applied to explain my design work related to simulation and study of an AC-AC converter topology along with a principle goal of improved power quality management. Content of this document is enriched by the way of adding suitably tabulations, graphics and schematics for the reader to easily digest.

Keywords— Pulse Width Modulation(PWM), SAF(Shunt Active Filter), Sinusoidal Pulse Width Modulation(SPWM), Total Harmonics Distortion(THD)

#### 1. INTRODUCTION

Normally, various loads in industrial and domestic applications need electrical power in the form of dc or a variable/high frequency ac. These loads are mostly nonlinear in nature. In addition to nonlinearity offered by loads, switching converters inherently contribute additional nonlinearity to the utility grid. Since utility grids offer electrical power by sourcing alternating sinusoidal voltage at 50 Hz, it is necessary to convert it by suitable power converter in required form. Unfortunately, input current drawn from source becomes either distorted or nonsinusoidal, depending upon a topology of front end of the converter and/or nonlinearity of load.

For understanding the issue, consider a 1 phase bridge rectifier with a non-linear load R and C in parallel as shown in Fig.1.When AC voltage is applied to the diode bridge, this voltage is rectified by the bridge, and the capacitor (C) charges to near the peak of the input AC voltage wave.

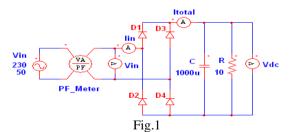


Fig.2 shows that the analysis of power factor and THD.

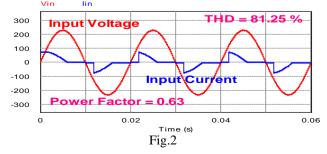
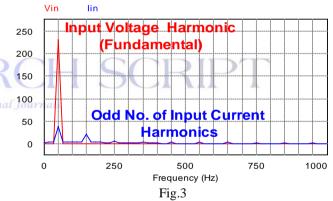


Fig.3 shows that, while converting utility ac power, source current must be approaching sinusoidal shape in order to minimize harmonic power reflected to grid. The deviation in current shape is a qualitative measure of harmonics of higher order other than the fundamental.



while converting utility ac power, source current must be approaching sinusoidal shape in order to minimize harmonic power reflected to grid. The deviation in current shape is a qualitative measure of harmonics of higher orders other than a fundamental.

## 2. HARMONICS REDUCTION TECHNIQUES FOR POWER QUALITY MANAGEMENT

#### A. Power Quality Management

A simpler and perhaps more concise definition might state: "Power quality is a set of parameters defining the properties of power quality as delivered to the user in normal operating condition in terms of continuity of supply and characteristic of voltage." The term Power quality is used to describe the extent variation of voltage, current and frequency in the power system. The variation of voltage and current can either be in terms of magnitude or distortion<sup>[1]</sup>.



## IJREE - International Journal of Research in Electrical Engineering

## B. Factor Deciding Power Quality

Depending upon the following factors Power Quality is decided;

- Voltage Stability
- Continuity of Supplying of Power
- Voltage Waveform

## C. Harmonics Reduction Techniques

A harmonic component in an AC power system is defined as a sinusoidal component of a periodic waveform that has a frequency equal to an integer multiple of the fundamental frequency of the system. Basically harmonic Reduction Techniques classified in to two main groups:

- i. Devices for cancellation of existing harmonics
  - Filters
  - Mixing Single and Three Phase Diode Rectifier
- ii. Grid friendly devices, which do not generate harmonics
  - Multi pulse Rectifier
  - PWM Rectifier

Table No.1 shows that the comparison of various techniques for selecting a particular method.

| Table No.1                                 |                               |   |  |                        |
|--|-------------------------------|---|--|------------------------|
|  | Harmonic Reduction Techniques |   |  |                        |
| Parameters                                 | Filters                       | Mixed<br>Single<br>and<br>Three-<br>phase<br>Non-<br>linear<br>load | Multiples<br>Rectifier                     | PWM<br>Rectifier       |
| Power<br>Factor                            | Unity                         | Not<br>Unity  | Nearly<br>unity                            | Unity                  |
| THD  | Nearly<br>5%                  | Above 5<br>% (7 to<br>11%)  | depends<br>on no. of<br>pulse<br>rectifier | Below 5<br>%           |
| Power Flow                                 | Conve<br>ntional              | Convent<br>ional  | Conventio<br>nal                           | Bi-<br>Direction<br>al |
| Efficiency                                 | High                          | Low   | Low  | High                   |
| Voltage<br>Drop                            | Low                           | Higher  | Higher                                     | Very<br>Less           |
| Control<br>strategy                        | Compl<br>ex                   | NIL   | NIL  | Simple                 |
| Switching<br>Losses                        | High                          | NIL   | NIL  | Low                    |
| Practical<br>Implementa<br>tion            | Easy                          | Comple<br>x   | Easy                                       | Complex                |
| Harmonic<br>content of<br>Input<br>current | Not<br>Present                | Present   | Present                                    | Not<br>Present         |

## 3. SIMULATION & RESULTS

For the reduction of harmonics PWM method is the better solution.

# A. Controlled AC to DC converter:

Fig.4 shows an active single phase AC to DC converter for a nonlinear load having R and C in parallel. **Circuit Operation:** Circuit diagram consists of 4 IGBTs, which form a full bridge, the input inductance and the capacitor at the output. IGBTs are triggered by applying pulse width modulated signals. Using a bipolar PWM switching strategy; this converter may have two conduction states <sup>[2]</sup>:

- I. Transistors T1 and T4 in the on-state and T2 and T3 in the off-state:
- II. Transistors T2 and T3 in the on-state and T1 and T4 in the off-state.

In this topology, the output voltage  $V_0$  must is higher than the peak value of the ac source voltage, to ensure a proper control of the input current.

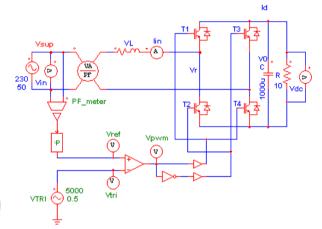
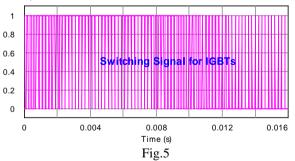
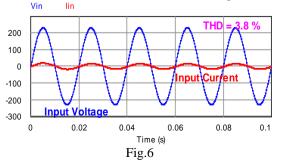


Fig.4

PWM rectifier with IGBTs is now simulated for its input current and results for THD, Power Factor and FFT analysis are shown in Fig.5



The input current shape approaches a sine wave and simulator shows values for current harmonics and Power Factor as mentioned, which is shown in the Fig.6.

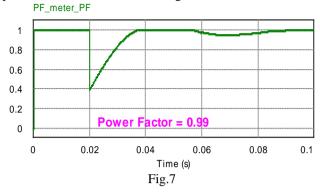


RESEARCH SCRIPT

#### IJREE - International Journal of Research in Electrical Engineering

ISSN: 2349-2503

It is to be noted that a Power Factor is approximately unity and no higher order odd current harmonics are present, which is shown in the Fig.7.



## B. Single Phase DC-AC Inverter

Fig. 8 shows the model of single phase full bridge DC-AC converter is made with all the parameter are assumed. A unipolar SPWM technique is used for a biasing of IGBTs. Input reference signal of comparator 2 is 180<sup>0</sup> out of phase with respect to reference signal of comparator 2.Primary of the Step-up transformer is connected at output of the Inverter and secondary side load is connected.

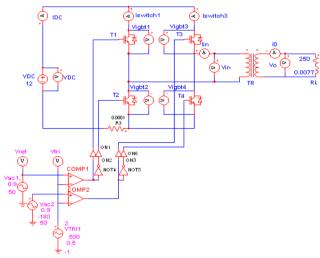
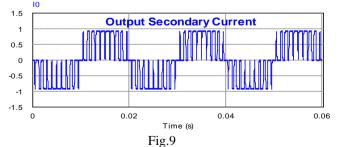


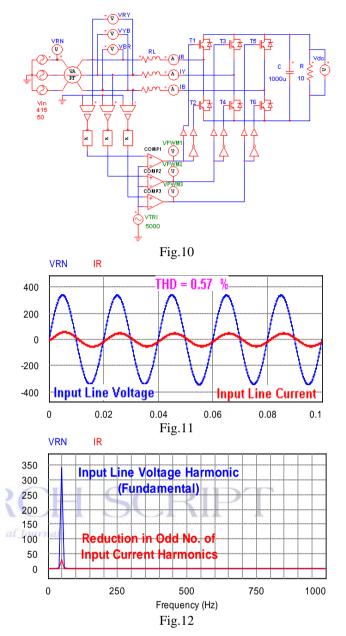
Fig.8

In the Fig.9 shows that the output waveform of single phase DC-AC inverter.



## C. Three phase AC-DC converter:

Fig.10 shows the fully Current Controlled Three Phase AC-DC Converter using Bipolar SPWM technique (refer Ch-4).Resistive load is connected across capacitive load at output. For smooth waveform of input current and output voltage proper filtering is used at input and output.



## D. Three phase DC-AC Inverter:

Consider a three phase full bridge DC-AC inverter as shown in Fig.13.

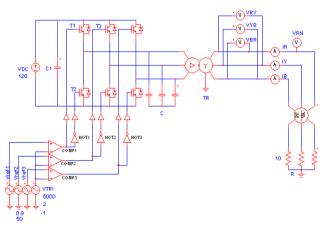
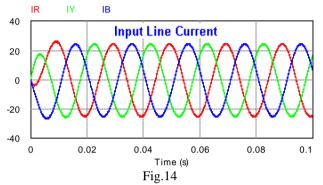


Fig.13

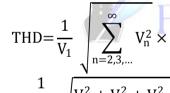


To obtain a set of balanced line-to-line output voltages, the switching sequence of switches  $S_1$ - $S_6$  should produce a sequence of pulses whose summation at any given time is zero. Filter is widely used in power electronics to reduce harmonic components at the output waveform. It is also used for smoothing the voltage wave of a load fed from a rectifier in reducing the harmonic content of an inverter output , preventing unwanted harmonic component being reflected into AC system and eliminating RF interference. Output of the inverter is a "chopped DC voltage with zero DC components" <sup>[6]</sup>. In certain types of application, "high purity" sine wave output is required such as UPS, which is shown in the Fig.14.



Also shows in the Fig.15 the Simulation Results for THD, Power Factor and FFT analysis.

**Total Harmonic Distortion, THD:** A measure of closeness in shape between a waveform and its fundamental component<sup>[5]</sup>.



International Tournal

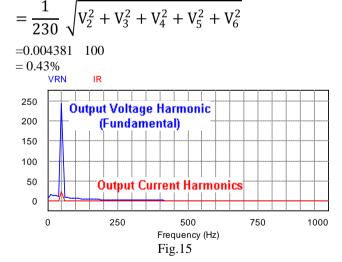
based true bi-directional AC switch is operated by PWM control to stabilize rms value of output voltage. Main focus of this work is on the insertion of harmonics for very high order. Without aiming any mitigation method, it is shown that distortion of mains current at high frequency in repeated order generates so many harmonics.

#### ACKNOWLEDGMENT

We would like to gratefully and sincerely thank to my colleague for guidance, understanding, patience, and most importantly help. We would like to thank the Institute for Plasma Research (IPR), who has given an opportunity to work. We owe them and promise that we will develop our lovely country together.

## REFERENCES

- "PWM Rectifier with Active Filtering" M. Sc. Mariusz Cichowlas Warsaw University of Technology Faculty of Electrical Engineering Institute of Control and Industrial Electronics Ph.D. Thesis
- [2] "Single Phase PWM rectifier" by Ing. Jan Bauer. Department of Electric Drives and Traction, Czech Technical University in Prague, Faculty of Electrical Engineering, Technická 2 166 27 Praha, Czech Republic
- [3] "Design and Development of Unipolar PWM Switching Pulses for SINGLE PHASE FULL BRIDGE INVERTER APPLICATION" by BAHARUDDIN BIN ISMAIL
- [4] "Microcontroller Based Sinusoidal PWM Inverter for Photovoltaic Application", S. M. Mohaiminul Islam, Gazi Mohammad Sharif School of Engineering and Computer Science, Independent University, Bangladesh
- [5] Second Edition Power Electronics Handbook Devices, Circuits and Applications by Muhammad Rashid. Page(s): 195-198
- [6] N. Mohan, T. Undeland & W. Robbins (1995) Power Electronics: Converters, Applications and Design, 2nd Edition, Wiley, New York.



## 4. CONCLUSION

By simulating and real time implementation the no. of harmonic order are very high. This is still issue for researcher to found the harmonic reduction techniques for this converter. A novel experiment was also carried out to realize a heater power supply at lower rating with "Uncommon AC to AC converter topology". An IGBT